

FIG. 1

2/10

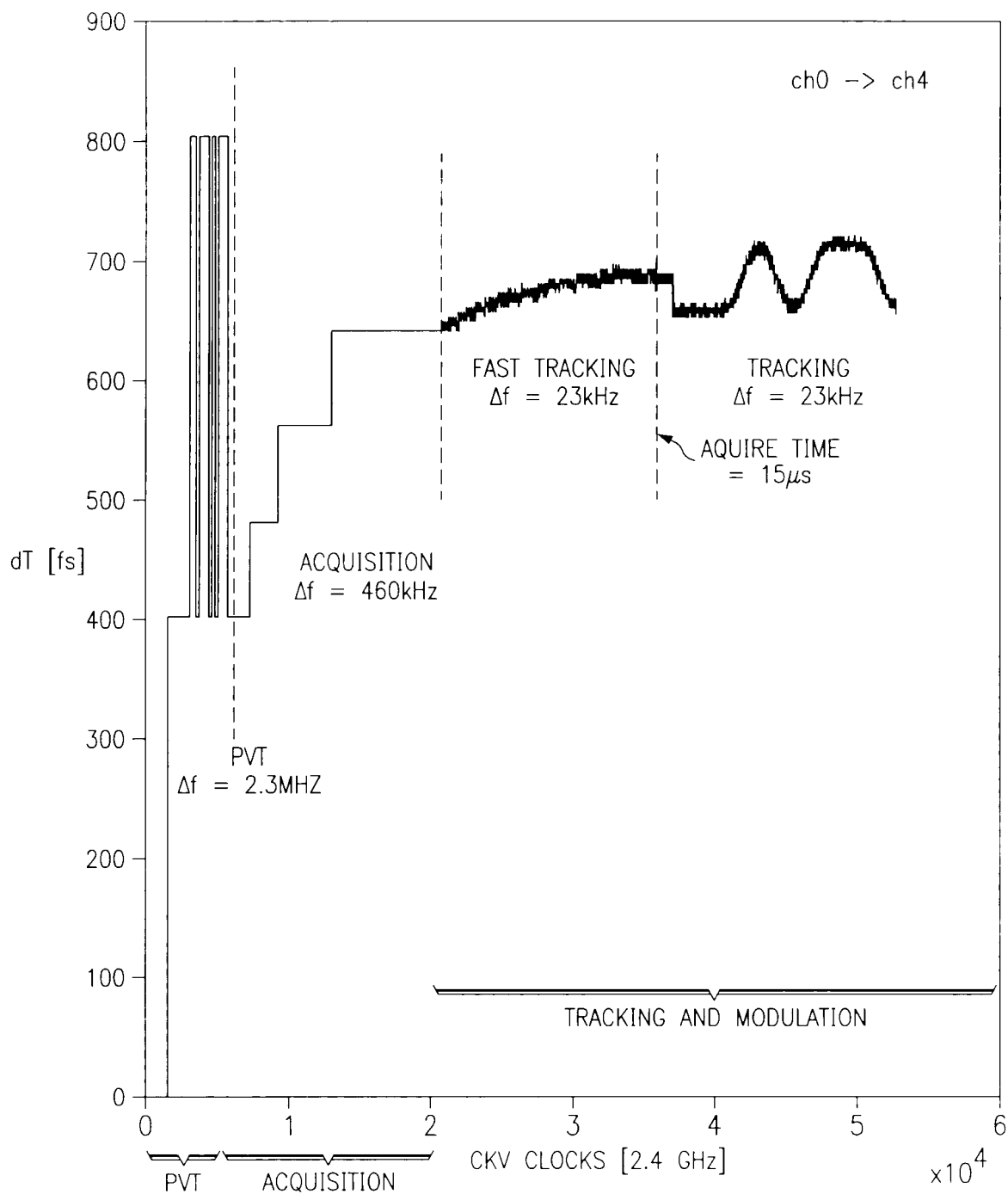


FIG. 2

3/10

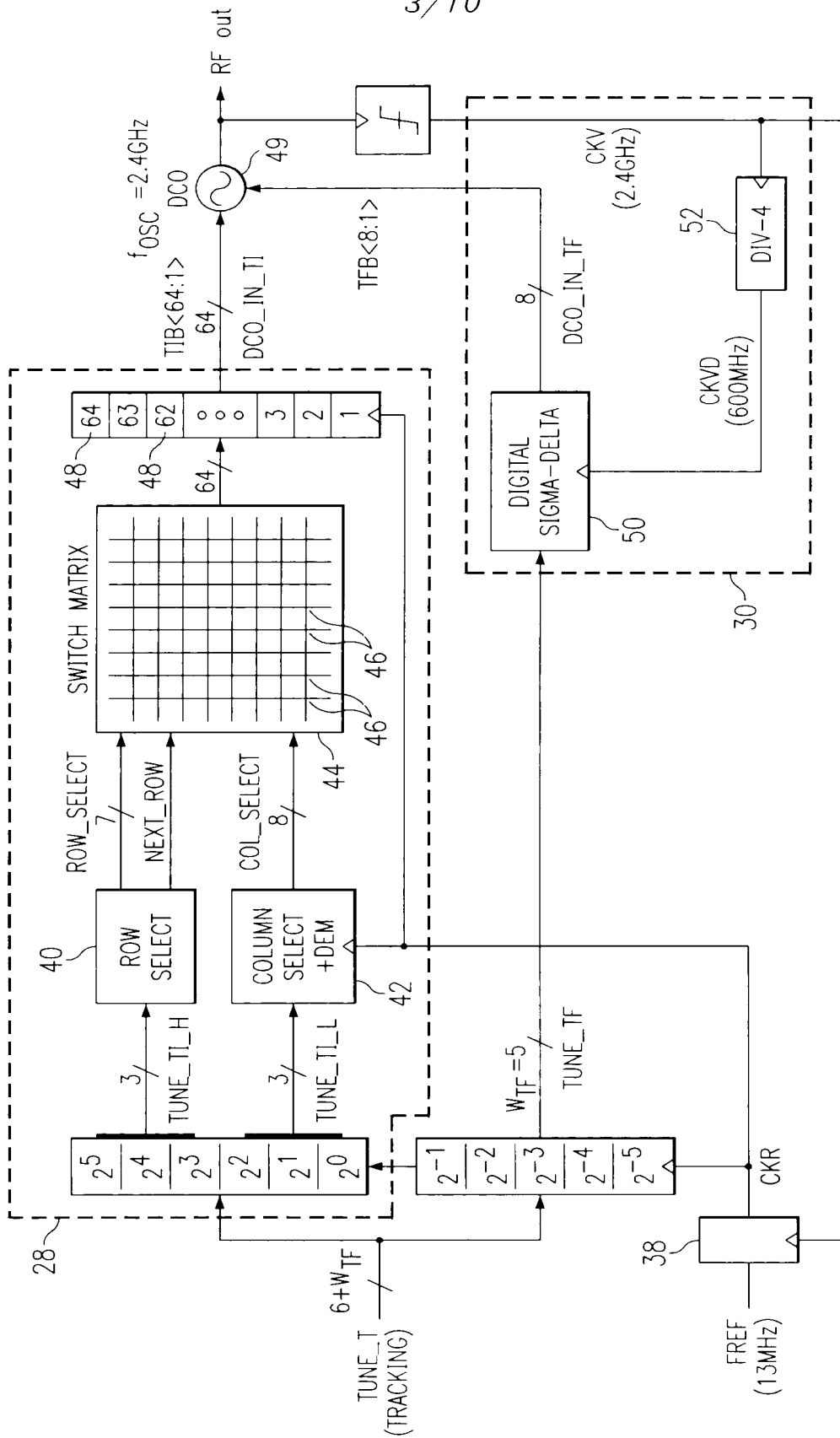
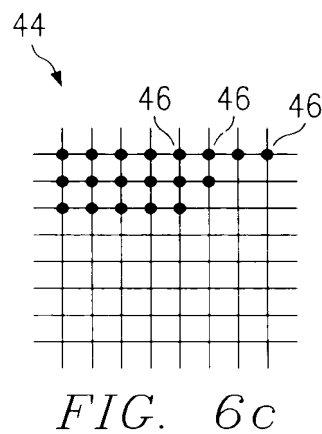
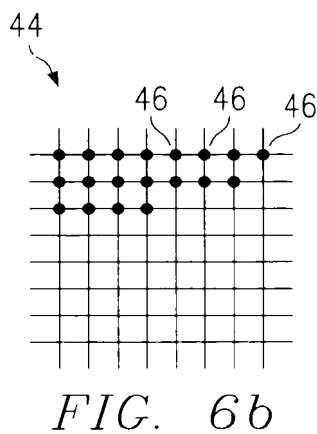
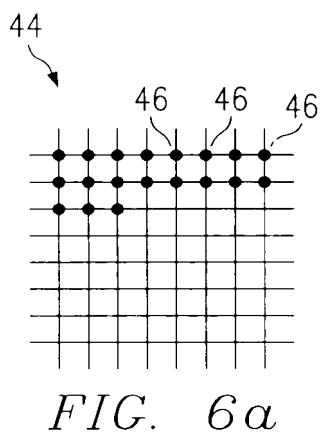
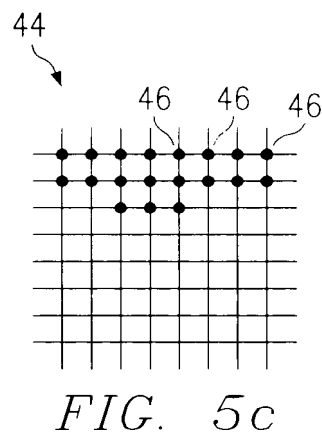
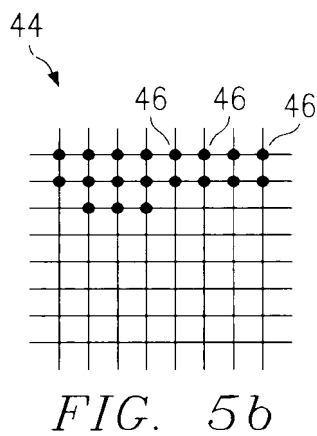
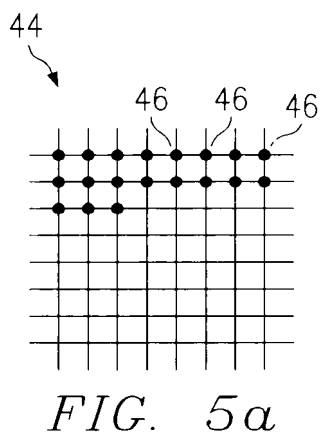
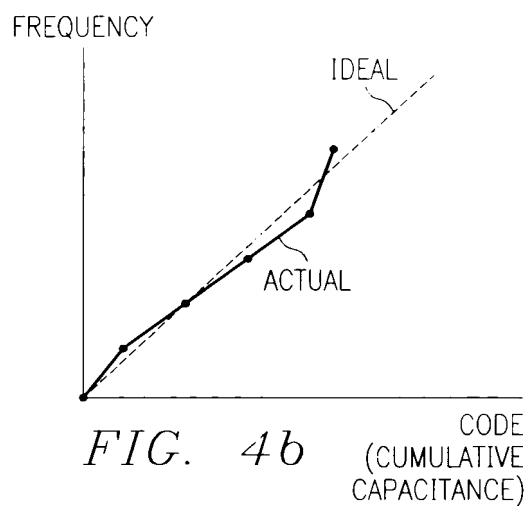
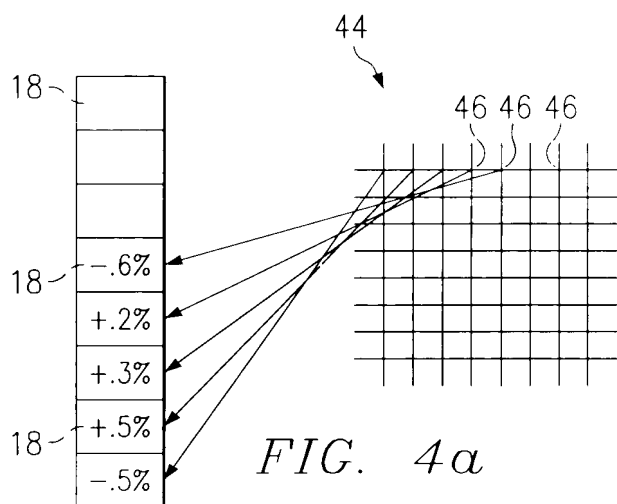


FIG. 3

4/10



5/10

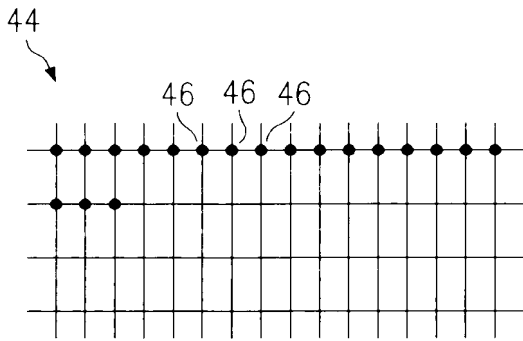


FIG. 7a

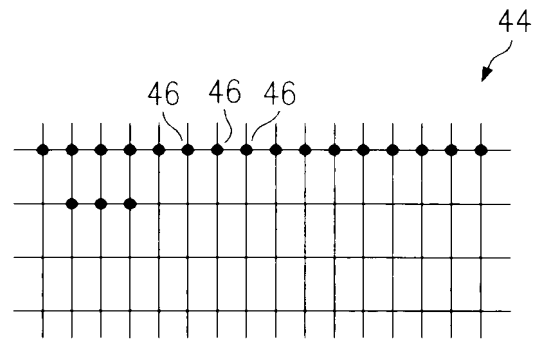
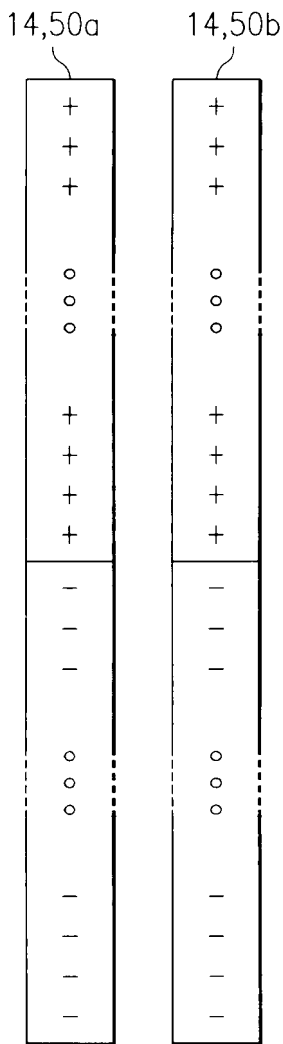
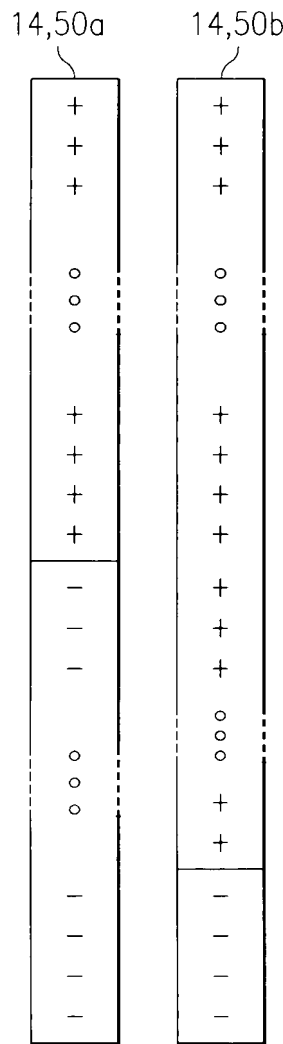


FIG. 7b



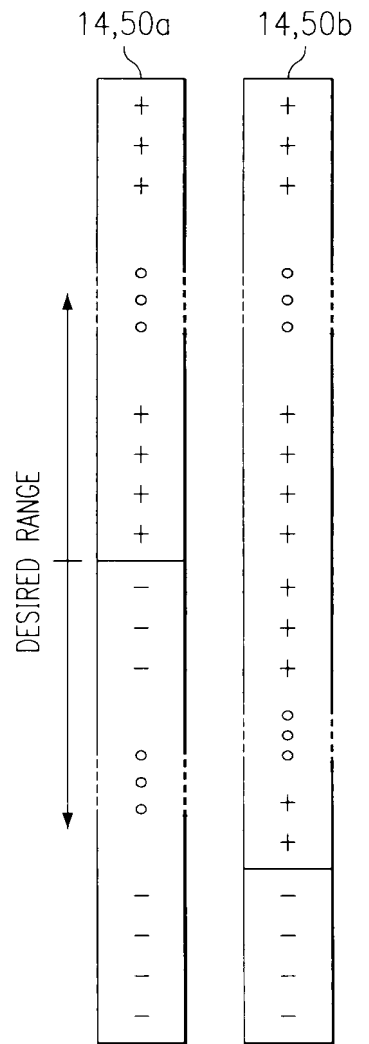
INITIAL STATE

FIG. 9a



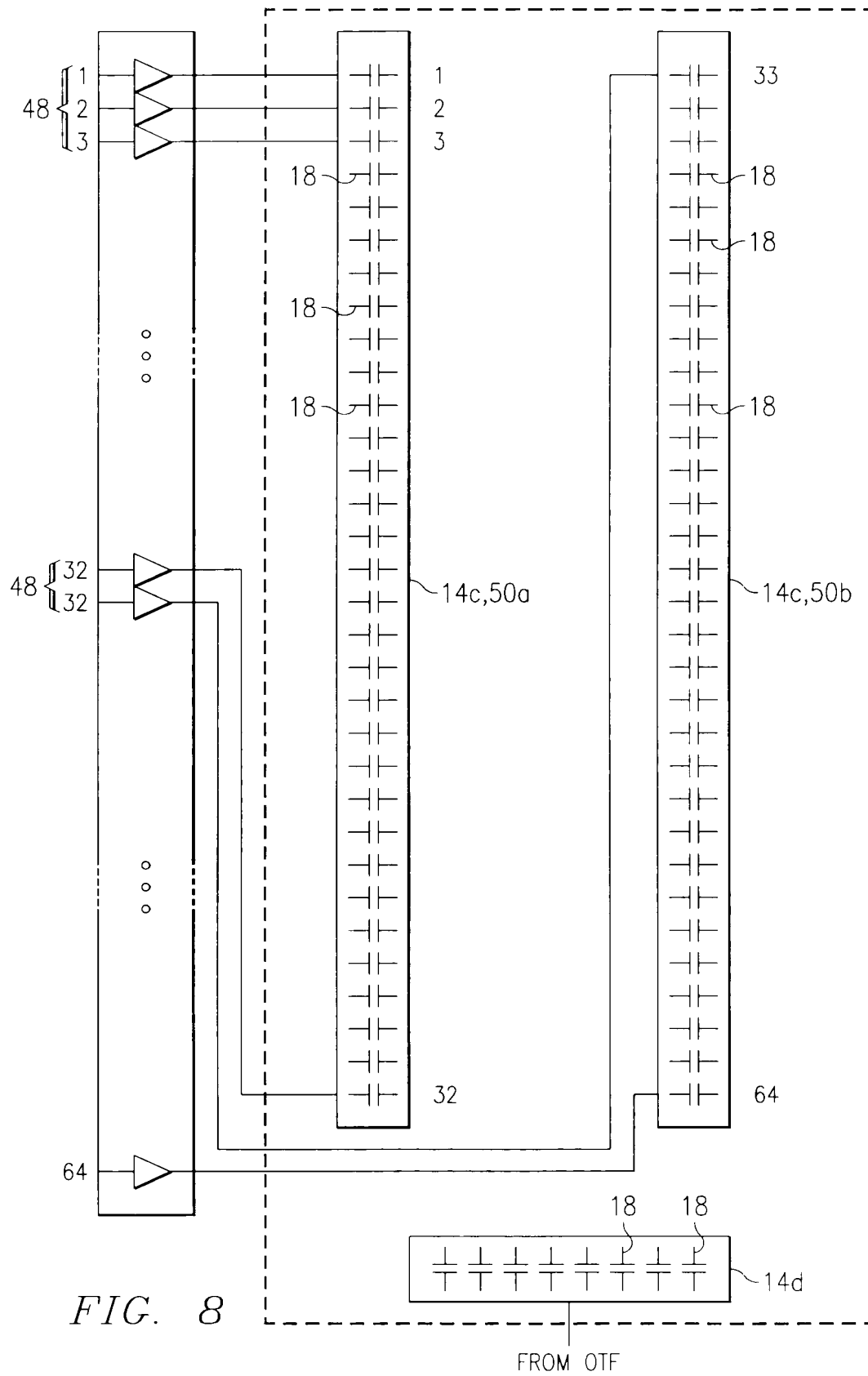
CHANNEL TUNING

FIG. 9b

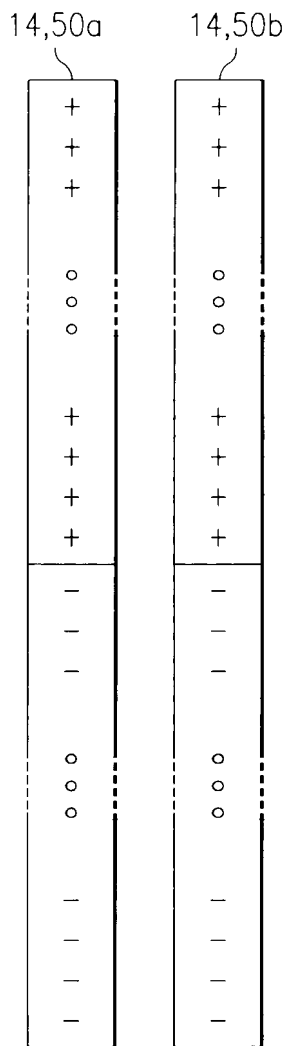


MODULATION AND DRIFT

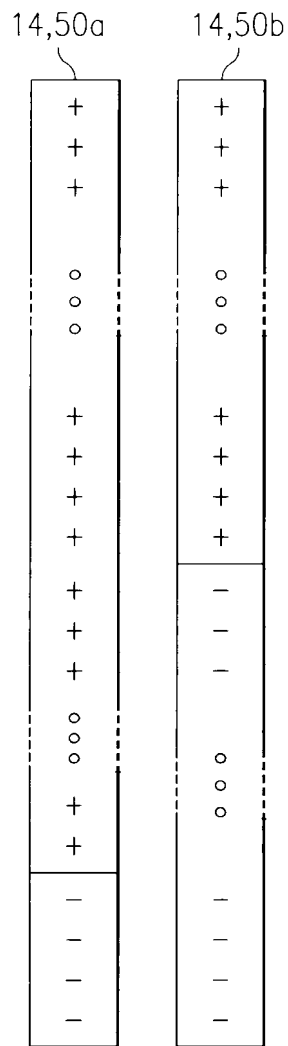
FIG. 9c



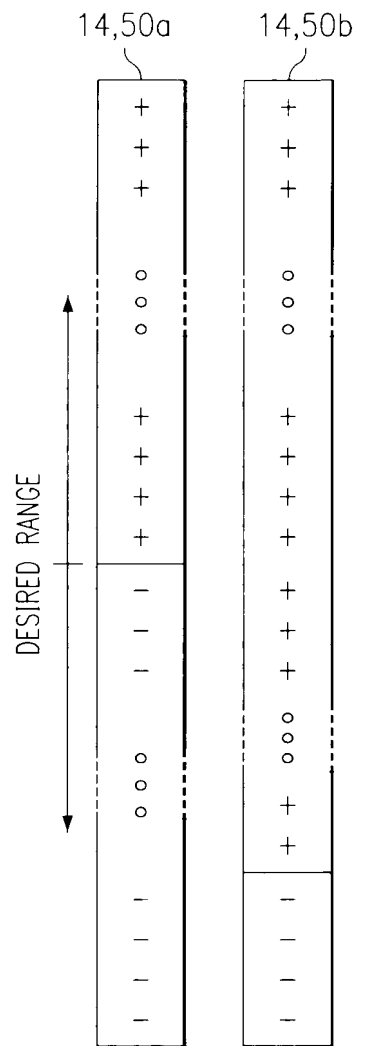
7/10



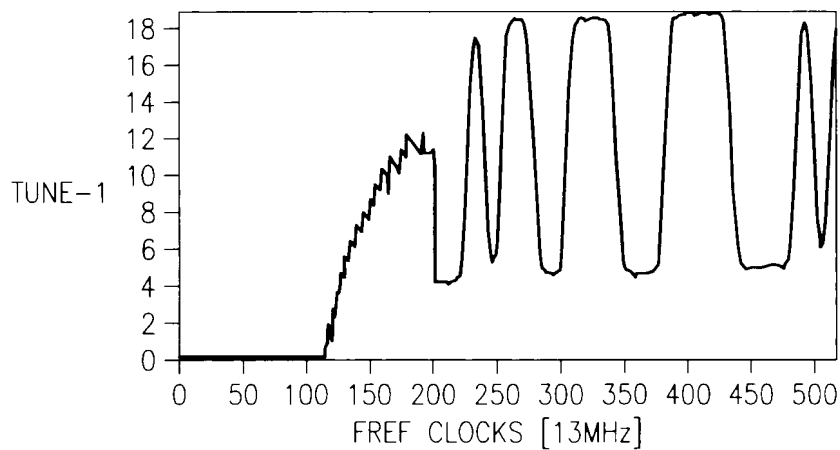
INITIAL STATE  
*FIG. 10a*



CHANNEL TUNING  
*FIG. 10b*



MODULATION AND DRIFT  
*FIG. 10c*



*FIG. 11a*

8/10

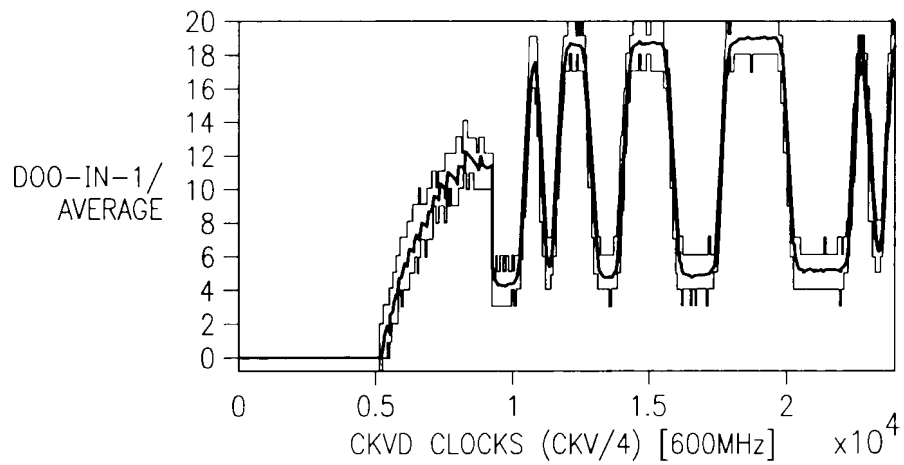


FIG. 11b

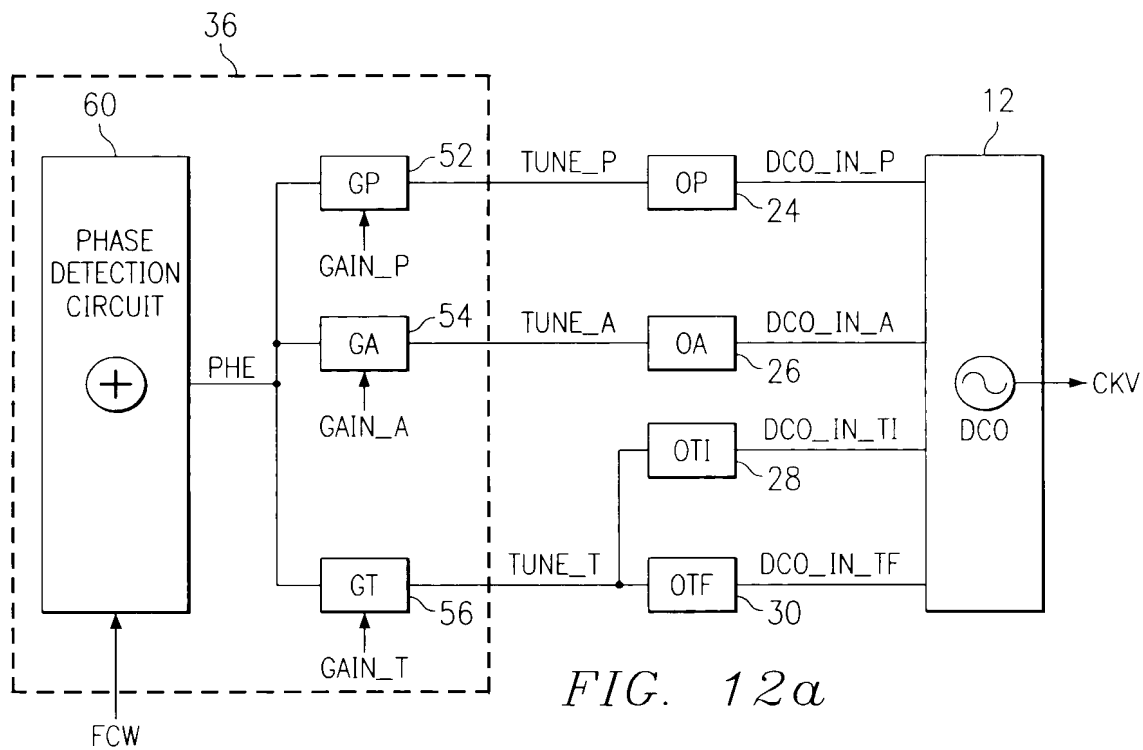
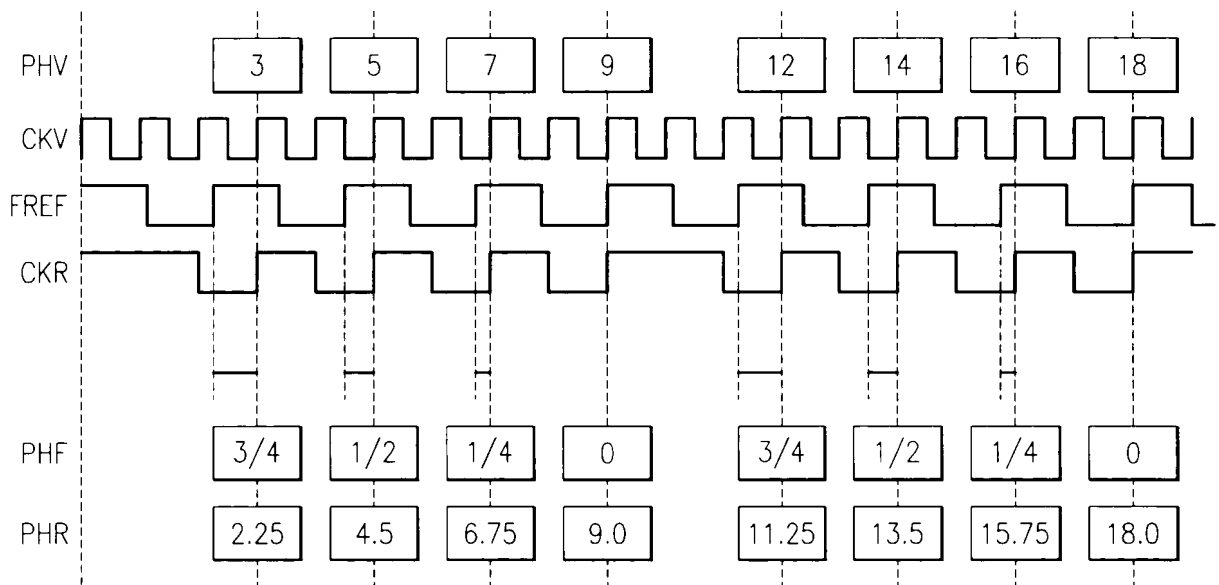
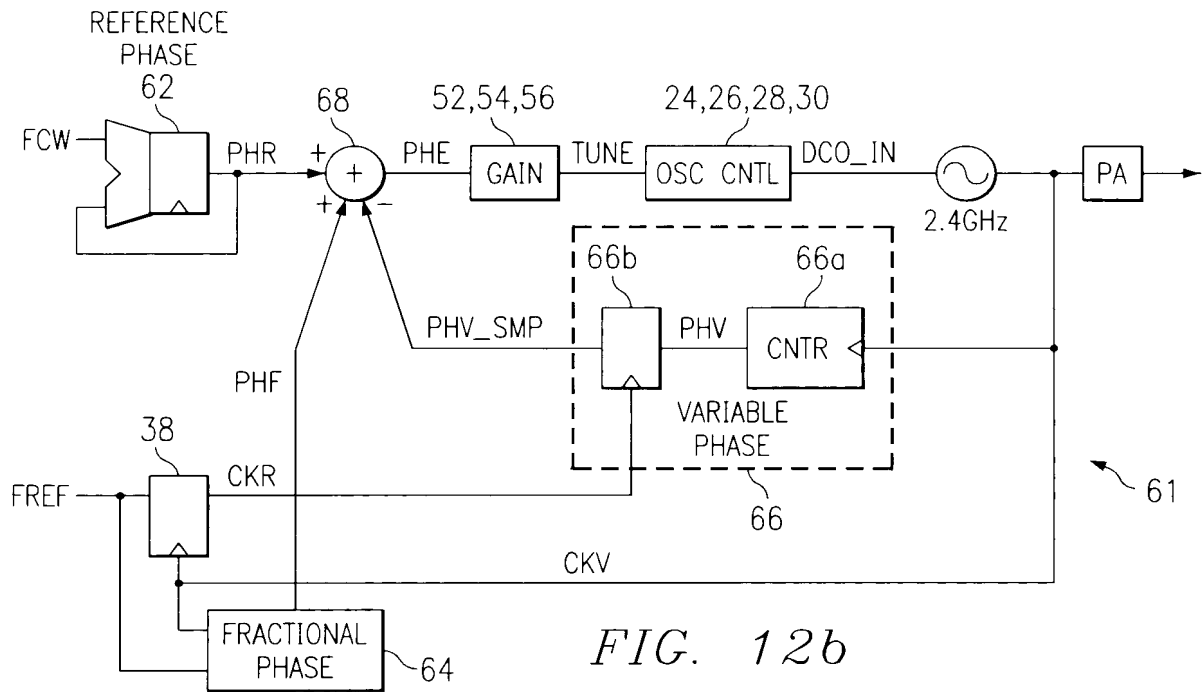


FIG. 12a



9/10



10/10

FIG. 13

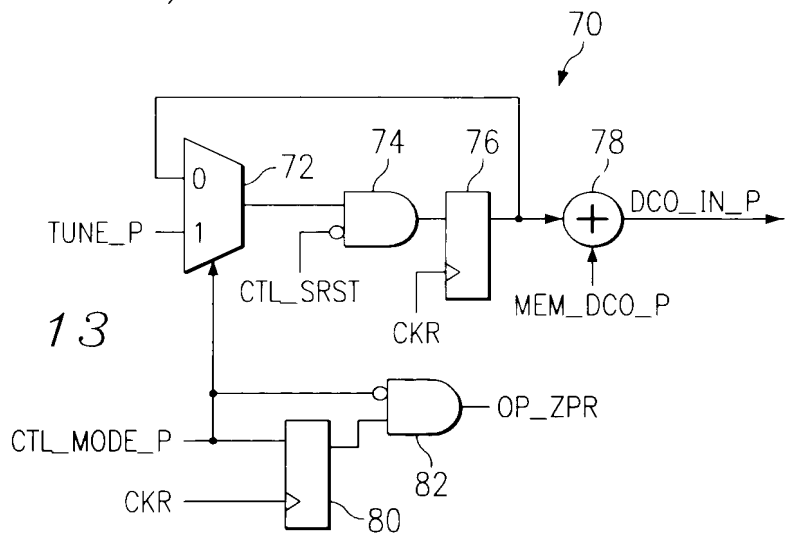


FIG. 14

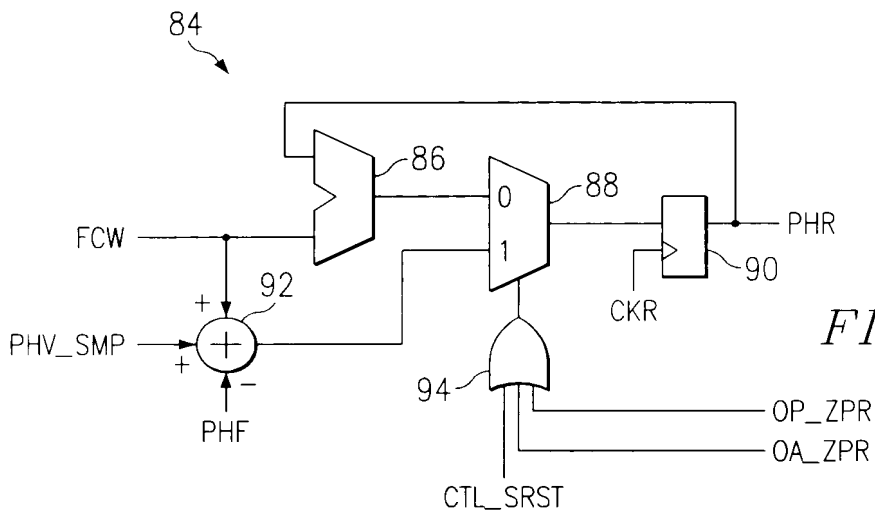


FIG. 15

